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U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub- Class	Filing Date

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub- Class	Transl.	
						Yes	No

OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)

<i>AW</i>	"A 6-GHz Integrated Phase-Locked Loop Using AlGaAs/GaAs Heterojunction Bipolar Transistors", By Aaron W. Buchwald et al., IEEE Journal of Solid-State Circuits, Vol. 27, No. 12, December 1992, pp. 1752-1762.

Examiner <i>Julia E. Zenger</i>	Date Considered <i>4/28/03</i>
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Examiner: Initial if references considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.